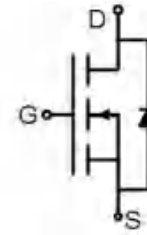


# AP80N04Q

## N-Channel Enhancement Mosfet

### Feature

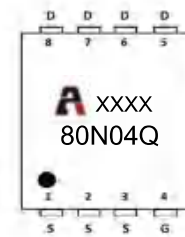
- 40V,40A  
 $R_{DS(ON)} < 7.5m\Omega @ V_{GS}=10V$  TYP 5.7 m $\Omega$   
 $R_{DS(ON)} < 14m\Omega @ V_{GS}=4.5V$  TYP 10 m $\Omega$
- Advanced Trench Technology
- Lead free product is acquired
- Excellent  $R_{DS(ON)}$  and Low Gate Charge



Schematic Diagram

### Application

- PWM applications
- Load Switch
- Power management



Marking and pin Assignment

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity (PCS)
80N04Q	AP80N04Q	PDFN3X3-8L	13 inch	-	5000

### ABSOLUTE MAXIMUM RATINGS ( $T_a=25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current ( $T_a=25^\circ\text{C}$ )	$I_D$	40	A
Continuous Drain Current ( $T_a=100^\circ\text{C}$ )	$I_D$	27	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	160	A
Singel Pulsed Avalanche Energy <sup>(2)</sup>	$E_{AS}$	81	mJ
Power Dissipation	$P_D$	21	W
Thermal Resistance from Junction to Case <sup>(4)</sup>	$R_{\theta JC}$	6	$^\circ\text{C}/\text{W}$
Junction Temperature	$T_J$	150	$^\circ\text{C}$
Storage Temperature	$T_{STG}$	-55~ +150	$^\circ\text{C}$

# AP80N04Q

N-Channel Enhancement Mosfet

## MOSFET ELECTRICAL CHARACTERISTICS( $T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Type	Max	Unit
<b>Static Characteristics</b>						
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	40	-	-	V
Zero gate voltage drain current	$I_{DSS}$	$V_{DS} = 40V, V_{GS} = 0V$	-	-	1	$\mu A$
Gate-body leakage current	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	-	-	$\pm 100$	nA
Gate threshold voltage <sup>(3)</sup>	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1	1.5	2.5	V
Drain-source on-resistance <sup>(3)</sup>	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 30A$	-	5.7	7.5	m $\Omega$
		$V_{GS} = 4.5V, I_D = 20A$	-	10	14	
<b>Dynamic characteristics</b>						
Input Capacitance	$C_{iss}$	$V_{DS} = 20V, V_{GS} = 0V, f = 1MHz$	-	2400	-	pF
Output Capacitance	$C_{oss}$		-	187	-	
Reverse Transfer Capacitance	$C_{rss}$		-	167	-	
<b>Switching characteristics</b>						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = 20V, I_D = 30A, R_L = 1\Omega$ $V_{GS} = 10V, R_G = 3\Omega$	-	13	-	ns
Turn-on rise time	$t_r$		-	12	-	
Turn-off delay time	$t_{d(off)}$		-	38	-	
Turn-off fall time	$t_f$		-	9	-	
Total Gate Charge	$Q_g$	$V_{DS} = 20V, I_D = 30A,$ $V_{GS} = 10V$	-	37	-	nC
Gate-Source Charge	$Q_{gs}$		-	6	-	
Gate-Drain Charge	$Q_{gd}$		-	7	-	
<b>Source-Drain Diode characteristics</b>						
Diode Forward voltage <sup>(3)</sup>	$V_{DS}$	$V_{GS} = 0V, I_S = 1A$	-	-	1.2	V
Diode Forward current <sup>(4)</sup>	$I_S$		-	-	40	A

### Notes:

1. Repetitive Rating: pulse width limited by maximum junction temperature
2. EAS Condition:  $T_J = 25^{\circ}\text{C}, V_{DD} = 20V, R_G = 25\Omega, L = 0.5\text{mH}$
3. Pulse Test: pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$
4. Surface Mounted on FR4 Board,  $t \leq 10\text{ sec}$

**Test Circuit**

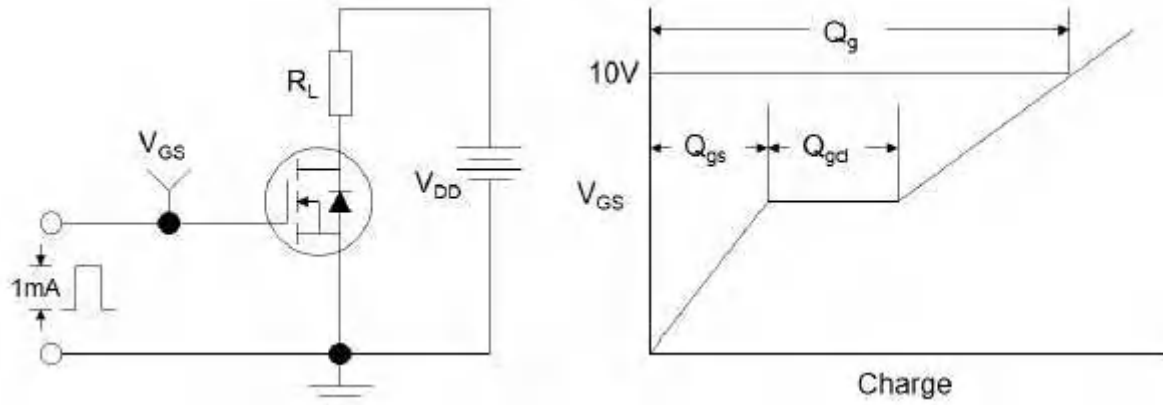


Figure1:Gate Charge Test Circuit & Waveform

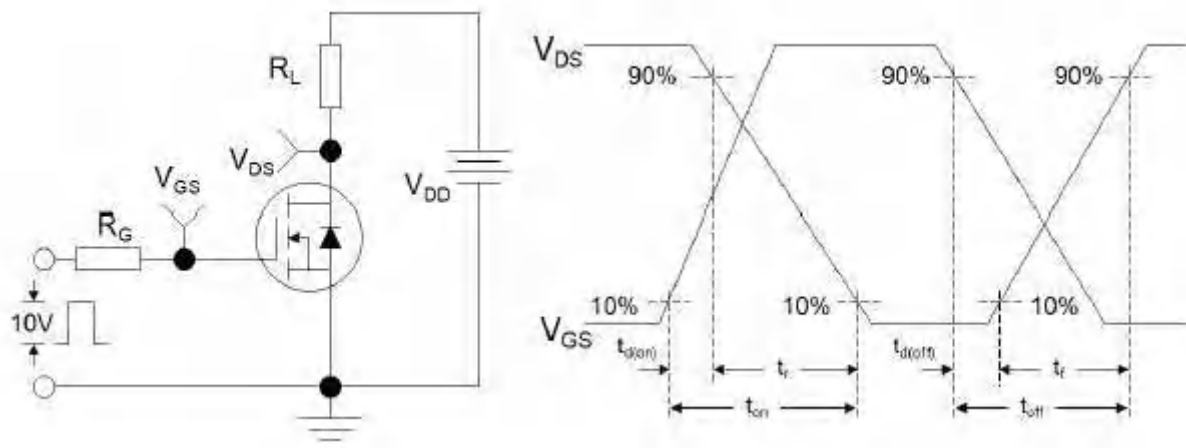


Figure 2: Resistive Switching Test Circuit & Waveforms

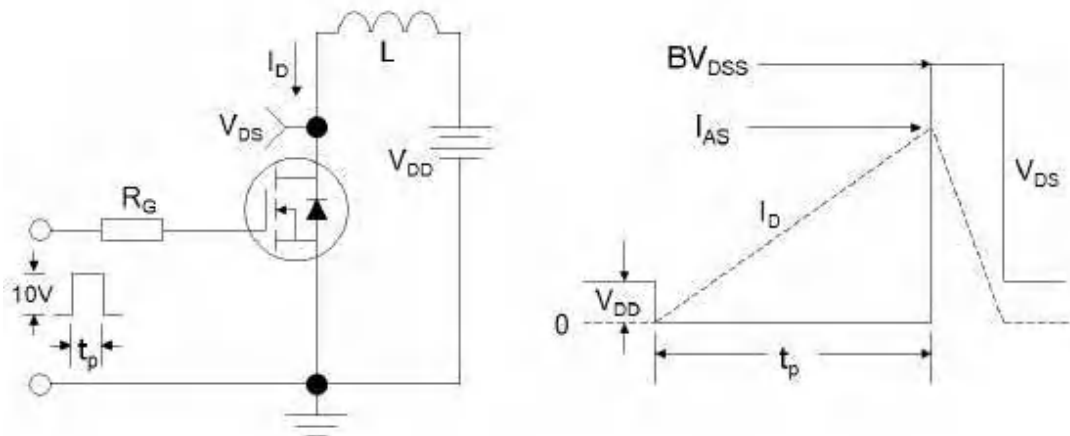
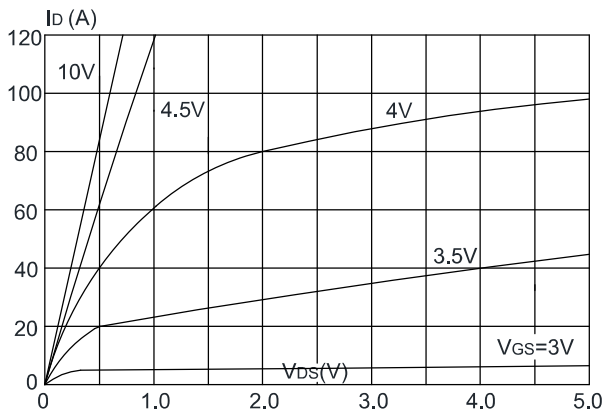
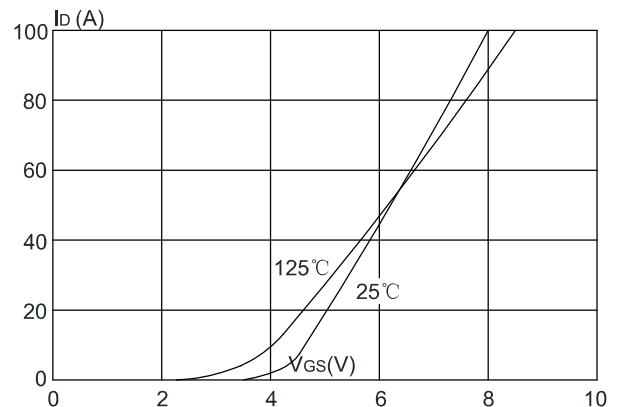


Figure 3:Unclamped Inductive Switching Test Circuit & Waveforms

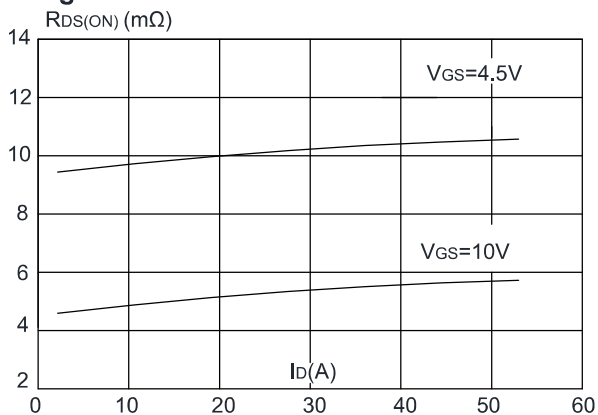
**Figure 1: Output Characteristics**



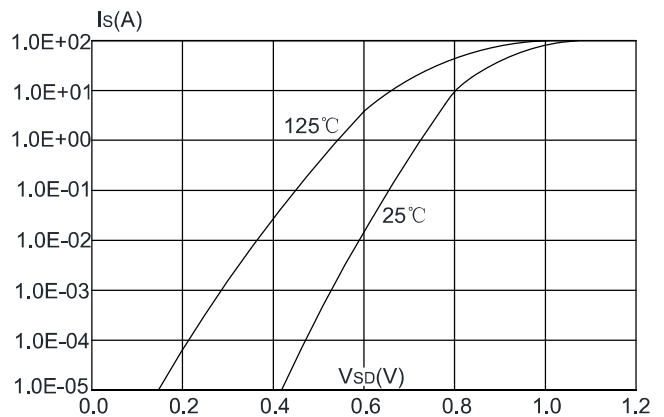
**Figure 2: Typical Transfer Characteristics**



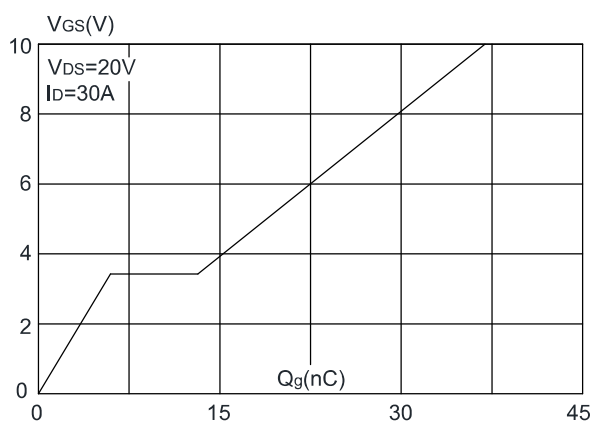
**Figure 3: On-resistance vs. Drain Current**



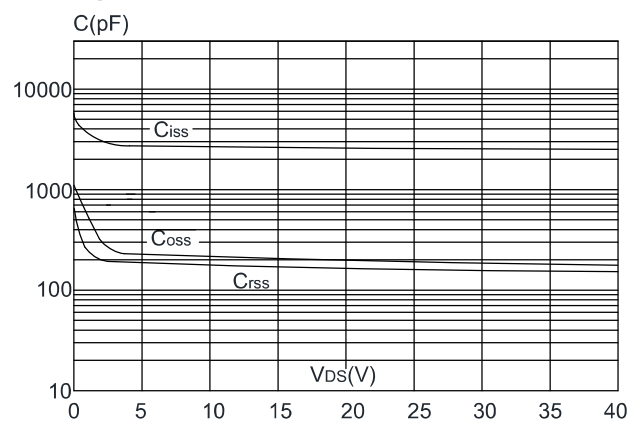
**Figure 4: Body Diode Characteristics**



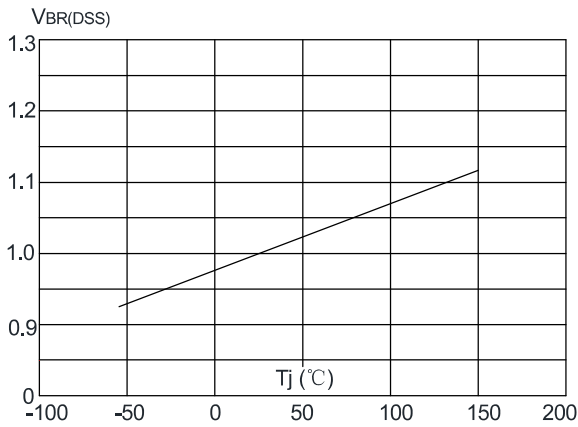
**Figure 5: Gate Charge Characteristics**



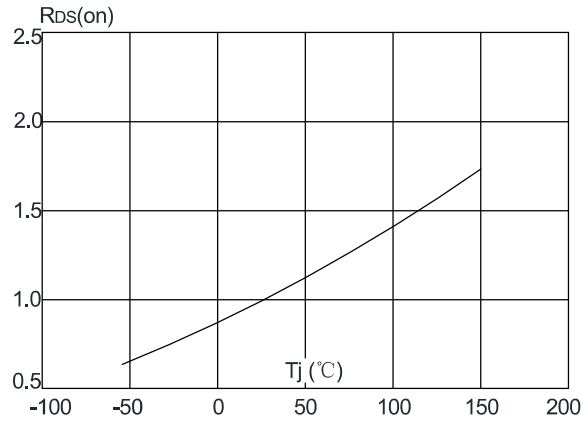
**Figure 6: Capacitance Characteristics**



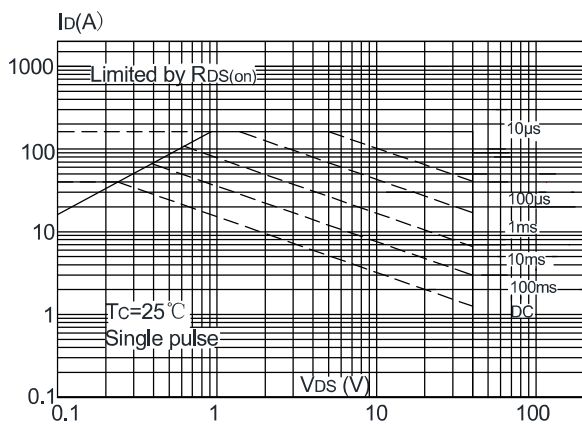
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



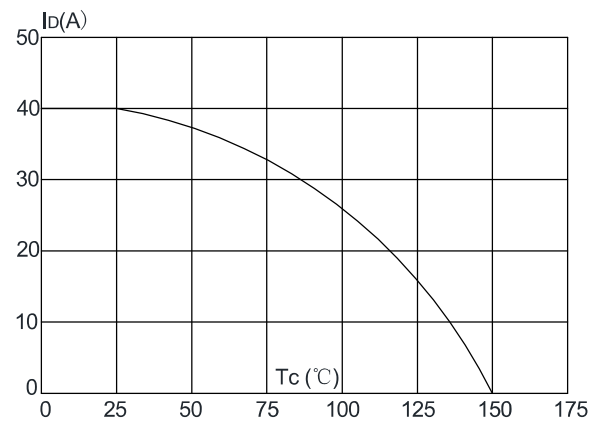
**Figure 8:** Normalized on Resistance vs. Junction Temperature



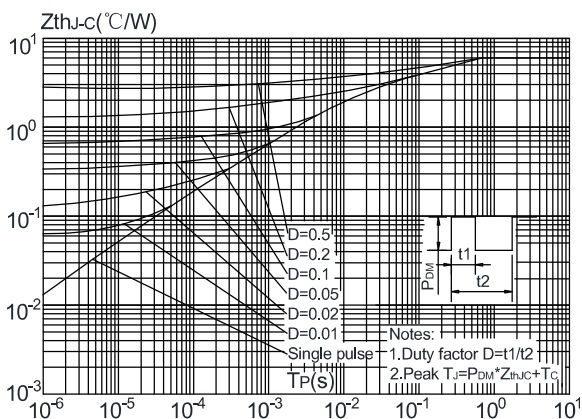
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature



**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**AP80N04Q**  
N-Channel Enhancement Mosfet

**PDFN3X3-8L Package Information**

