

□命名方法 Part Code Designation

CT81—6KV—07 b—2B4—101 K—2 T3
 ① ② ③ ④ ⑤ ⑥ ⑦ ⑧ ⑨

①種類 Class

代碼 Code	CT81
CT81	Ⅱ類高壓 Class II High-Voltage

②額定電壓 Rated Voltage

代碼 Code	6KV
額定電壓 Rated Vol.	6000V. DC

③主體外徑 Body Diameter

代碼 Code	07
最大外徑 Dmax	7.5

④引線形式 Lead Shape

代碼 Code	b
形式 Shape	直腳 Straight long

⑤溫度特性 Temperature Characteristic

代碼 Code	2B4(Y5P)
容量變化 Cap. Change	-10~+10%

⑥標稱容量 Rated Capacitance

代碼 Code	101
靜電容量 Capacitance	100pF

⑦容量允差 Tolerance

代碼 Code	K
容量允差 Tolerance	±10%

⑧引線間距 Lead spacing

代碼 Code	1	2	3
間距 spacing	5.0mm	7.5mm	10mm

⑨包裝方式 package Shape

代碼 Code	T3	無標識 No Mark
編帶形式 Taping Package Shape	P0=15.0mm P=15.0 mm	散件 bulk

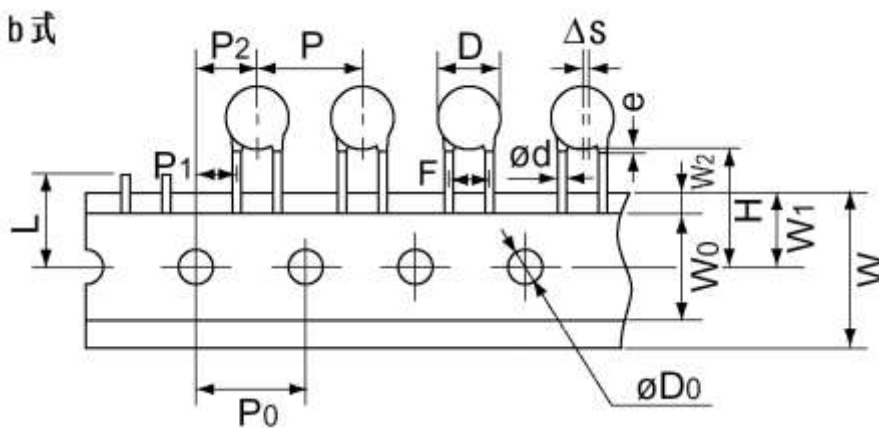
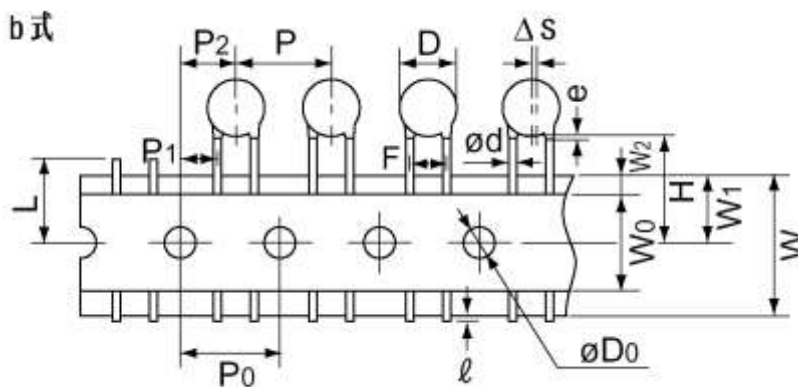
規格型號 Specification model

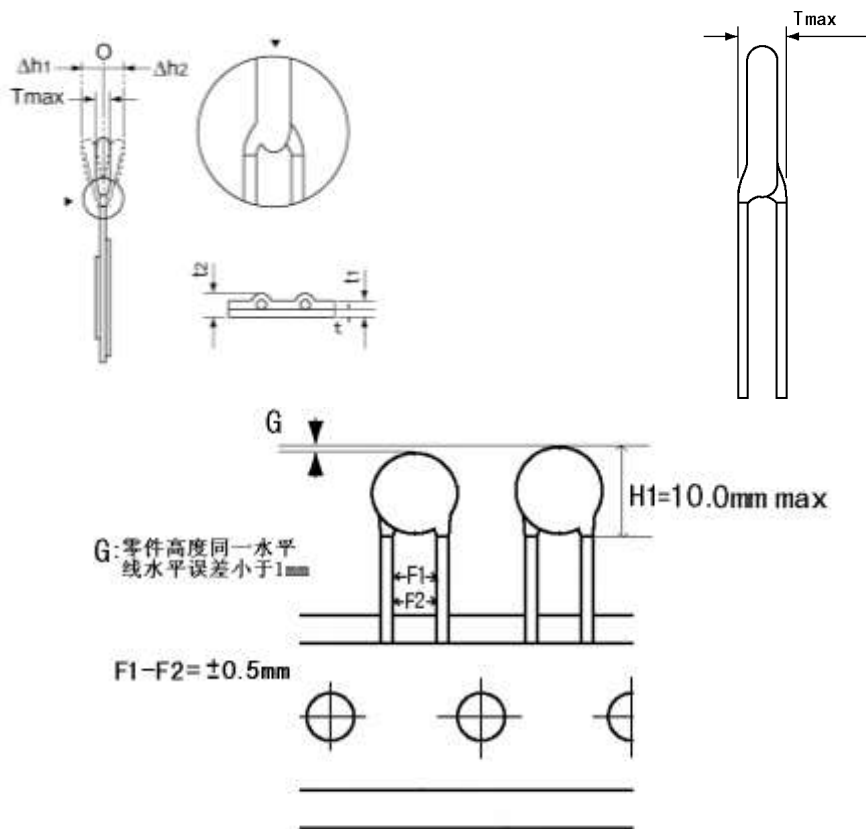
編碼 code	品名 (CODE NO.)	D_{max} (mm)	T_{max} (mm)	$F \pm 0.5$ (mm)	$d \pm 0.06$ (mm)
	CT81-6KV-07b-2B4-101K-2T3	7.5	6.0	7.5	0.6

編帶 (Taping Package):

- b 式 (引線間距 $F = 7.5\text{mm}$, 孔間距 $P_0 = 15.0\text{mm}$, 產品間距 $P = 15.0\text{mm}$)

Type b (lead spacing $F = 7.5\text{mm}$, Hole spacing $P_0 = 15.0\text{mm}$, Product spacing $P = 15.0\text{mm}$)





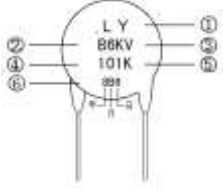
注: 左侧编带图为一二次编带, 右侧编带图为二次编带 (left banding diagram is primary banding ;right banding diagram is secondary banding.)

P0	F	H	Φd	$\Delta h1/\Delta h2$	ΔS	t1
15.0	7.5 ± 0.5	20 ± 2	0.6 ± 0.06	0 ± 1.0	0 ± 1.3	0.6 ± 0.3

Unit:mm

項目 Item	代碼 Code	尺寸 Dimensions (mm)
產品間距 Pitch of component	P	15.0 ± 1.0
傳送孔間距 Pitch of sprocket hole	P0	15.0 ± 0.3
傳送孔位置偏差 Position of sprocket hole	P1	3.75 ± 1.5
	P2	7.5 ± 1.3
紙帶寬度 Carrier tape width	W	18.0 ± 0.5
膠帶寬度 Hold-down tape width	W0	6.0min
傳送孔位置偏差 Position of sprocket hole	W1	9.0 ± 0.5
膠帶偏差 Hold-down tape distortion	W2	1.5 ± 1.5
傳送孔孔徑 Diameter of sprocket hole	D0	4.0 ± 0.2
不良切斷位置 Portion to cut	L	11.0 max
包封料垂延 Coating extension on lead	e	2.5max
紙帶厚度 thickness of tape	t	0.3-0.6
	t1	0.6 ± 0.3
	t2	1.5max
零件高度偏差 Part height deviation	G	1.0max
引線尾端長度 Lead end length		0max
產品片徑 Product diameter	Dmax	見第 5 頁: 規格型號 See page 4: Specification Type
產品厚度 Product thickness	Tmax	見第 5 頁: 規格型號 See page 4: Specification Type

□標識方法 (Marking)

示例(Example)	項目 (Item)	
	①. LY	LY:公司代號 ; LY:Manufacturer's Code ;
	②B	溫度特性 Temperature Characteristic
	③6KV	額定電壓 Rated Voltage
	④101	標稱容量 Rated Capacitance
	⑤K	容量偏差 Tolerance of Capacitance
	⑥8B6	生產日期 Production Date

說明：生產日期第一位表示製作年度，標示方法參照年度對照表；第二位表示製作月份，第三位表示具體製作日期，標示方法參照 34 進制對比表；例如：生產日期 8B6 表示：8:18 年 B:11 月，6:6 日

(Production date first said the annual, marking methods according to annual comparison table; second said production month, third said the specific production date marking method of comparison, table 34 hexadecimal; Example : Production date 8B6 show: 8:2018year B: november 6: the 6 day) :

年度對比表：

年 度	2013	2014	2015	2016	2017	2018	2019
年代碼	3	4	5	6	7	8	9
年 度	2020	2021	2022	2023	2024	以此類推	
年代碼	0	1	2	3	4		

34 進制對比表：

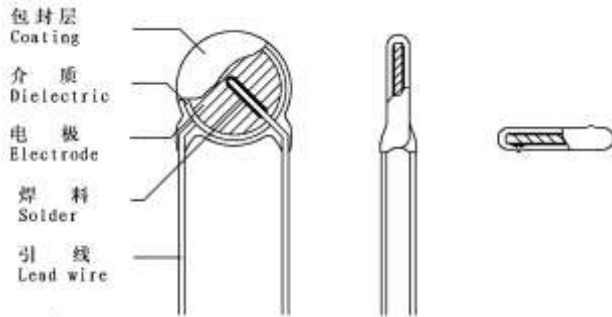
34 進制	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	H
10 進制	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
34 進制	J	K	L	M	N	P	Q	R	S	T	V	W	X	Y	Z		
10 進制	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31		

□用途 Application

該產品主要用於彩電、電腦顯示器、影印機、醫療設備、照明等的電源電路、輸出電路等部分。

Using for H-out and supply circuits of color TV and monitor、copy machine、Medical equipment、lighting.

結構(Structure)



包封層(Coating) : 環氧樹脂(Epoxy Resin)
 介質(Dielectric): 陶 瓷 (Ceramic)
 電極(Electrode : 銀 (Silver)
 焊料(Solder) : 錫(Alloy Tin)
 引線(Lead Wire) : 鍍錫引出線(Lead)

主要材料(Main Material)

序號 NO.	材料名稱(Material name)	規格(Specifications)
1	介質 Ceramic core	CT81 DC
2	電極 Silver paste	SP-255PL SP-YD55
3	包封料 Expoxy powder coating	HXF-240 XCP-231 EF-150
4	鍍錫線 Tin-plated copper weld Steel wire	0.6
5	無鉛焊料 Unleaded solder	97%Sn 2.5%Ag 0.5%Cu

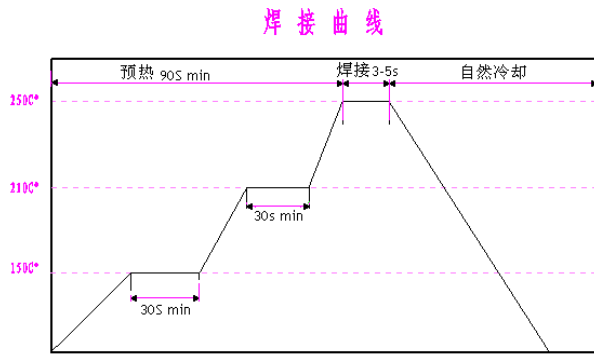
室內條件(Room Condition)

溫度(Temp.) : 15~35°C 濕度(R. H.) : 45~75%
 氣壓(Atm pressure) : 86~106kPa(860~1060mbar)

測試條件(Test Condition)

溫度(Temp.) : 20±2°C 濕度(R. H.) : 50~60%
 電壓(Vol.) : 1.0±0.2Vrms 頻率(Freq.) : 1±0.2KHz

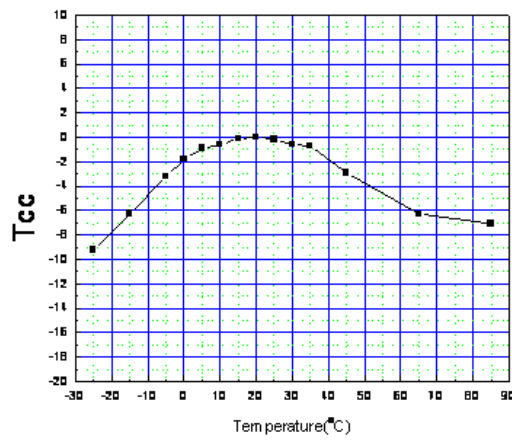
□ 焊接條件 (Soldering Condition)



手工焊 Hand-soldering	400±10°C	3-5s
波峰焊 Wave-soldering	260±5°C	3-5s

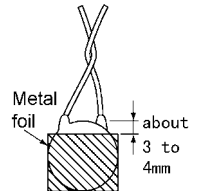
□ 容量—溫度變化曲線 Cap.—Temp. Curve

2B4(Y5P)

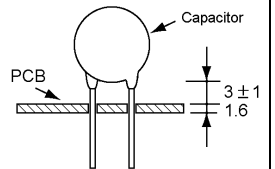
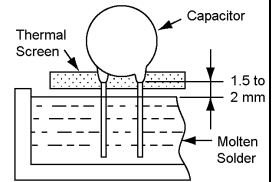
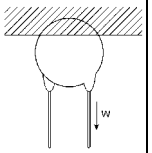


□規格及試驗方法 Specification and Test Method

項 目 ITEM		規 格 SPECIFICATION	試 驗 方 法 及 條 件 TEST METHOD AND CONDITION																									
1. 存儲溫度範圍 Storage Temp. Range		-40°C~+125°C																										
2. 使用溫度範圍 Operating Temp. Range		-25°C~+125°C																										
3. 外觀尺寸 Appearance and Dimension		外觀無可見損傷 尺寸在規格內 Appearance has no marked defect. Dimensions shall be within specified tolerance.	外觀用目視法觀測 尺寸用遊標卡尺測量 Appearance be watched on sight Dimension be measured by caliper																									
4. 標識 Mark		應清晰可見 Should be discerned easily.	用目視法觀測 Be watched on sight																									
5. 靜電容量 Capacitance		在規格範圍內 Within specified tolerance	溫度 Temp. 20±2°C 電壓 Vol. 1.0±0.2Vrms 頻率 Freq. 1±0.1KHz																									
6. 損耗因數 Dissipation Factor		B:2.5% max	同上 Same condition as capacitance																									
7. 絕緣電阻 Insulation Resistance		大於 10,000MΩ 10,000MΩmin	500±50V. DC 的電壓充電一分鐘。 The insulation Resistance shall be measured with 500±50V. DC within 60±5 sec of charging.																									
8. 耐電壓 Dielectric Strength	端子間 Between Lead Wires	無不良 No failure.	端子間施加 200%的額定電壓 5 秒。(1KV≤UR≤6.0KV : 200%) (充放電電流≤50mA) Apply a DC voltage of 200% of the rated voltage for 5sec. (1KV≤UR≤6.0KV : 200%) (Charge/discharge current≤50mA)																									
	端子與外殼間 Body Insulation	無不良 No failure.	如圖，將電容器的引線連在一起，主體外緊包一層金屬箔，邊沿距引線 3-4mm，在電容器引線和金屬箔間施加 1.3KVDC 電壓 5 秒。(充放電電流≤50mA) The terminals of the capacitor shall be connected together, A metal foil shall be closely wrapped around the body of the capacitor to the distance of about 3-4 mm from each terminal, A voltage of 1.3KVDC is applied between the capacitor lead wires and the metal balls for 5sec. (Charge/discharge current≤50mA)																									
9. 溫度特性 Temp. Char.	Ct-C3 C3	B:-10~+10%	靜電容量測試須依下列順序測試。 試驗前：電容器應放置在 85±2°C 的溫度下 1 小時，然後在常溫下恢復 24±2 小時後測試。 The capacitance measurement shall be made at each step specified as following. Capacitance change from the volume of step 1 shall not exceed the limit specified. pre-treatment: The capacitor shall be placed at 85±2°C for 1 hour, then placed at room condition for 24±2 hours before initial measurement.																									
			<table border="1"> <tr> <td>B</td> <td>步 驟 (Step)</td> <td>①</td> <td>②</td> <td>③</td> <td>④</td> <td>⑤</td> </tr> <tr> <td>E</td> <td>溫 度 (Temp.)</td> <td>20±2°C</td> <td>-25±2°C</td> <td>20±2°C</td> <td>85±2°C</td> <td>20±2°C</td> </tr> <tr> <td>F</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> <tr> <td>HR</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td></td> </tr> </table>	B	步 驟 (Step)	①	②	③	④	⑤	E	溫 度 (Temp.)	20±2°C	-25±2°C	20±2°C	85±2°C	20±2°C	F							HR			
B	步 驟 (Step)	①	②	③	④	⑤																						
E	溫 度 (Temp.)	20±2°C	-25±2°C	20±2°C	85±2°C	20±2°C																						
F																												
HR																												



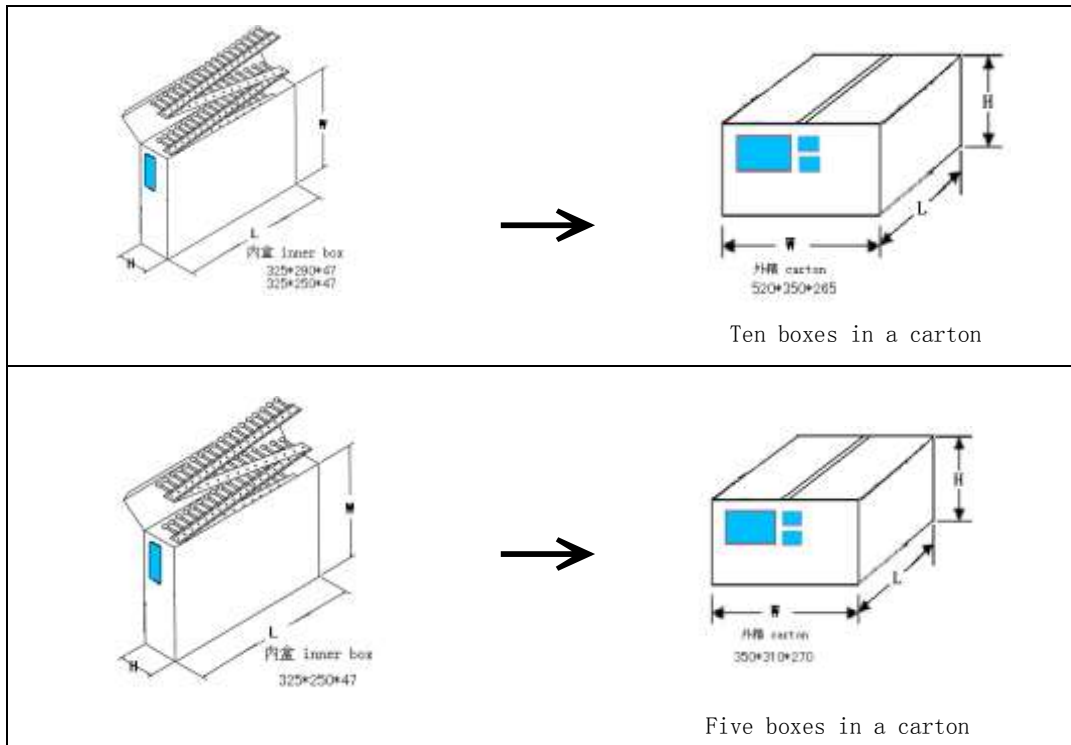
項 目 ITEM	規 格 SPECIFICATION		試 驗 方 法 及 條 件 TEST METHOD AND CONDITION															
10. 端子強度 Strength of Lead Wires (c 式不做此 項 Type c none)	抗拉強度 Pull	導線不斷裂 電容器不破損 Lead wire shall not cut off and capacitor shall not be damaged	把製品固定, 在端子引出方向施加負荷 10N 保持 10±1 秒。 Fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of capacitor up to 10N, and keep it for 10 ±1sec.															
	彎曲強度 Bending		在端子間施加 5N 負荷並彎曲 90°, 回復原後反向彎曲 90°, 每次彎曲時間為 2 至 3 秒, 連續 2 次。 Each lead wire shall be subjected to 5N weight and then a 90°bend, at the point of egress, in one direction return to original position, and then a 90°bend in the opposite direction at the rate of one bend in 2-3 s for 2times.															
11. 耐焊接熱 Soldering Effect	外觀 Appearance	無顯著異常 No marked defect	將端子浸入溫度為 260±5 °C 的熔錫內, 外保留 1.5-2.0mm 距離主體邊緣, 並保持 10±1 秒。															
	容量變化 Capacitance Change	B:±10% max	試驗前: 電容器應放置在 125±2°C 的溫度下 1 小時, 然後在常溫下恢復 24±2 小時後測試。 試驗後: 室內條件下恢復 24±2 小時。 The lead wires shall be immersed into the melted solder of 260±5 °C up to about 1.5-2.0 mm from the main body for 10±1 sec. Pre-treatment: The capacitor shall be placed at 125±2°C for 1 hour, then placed at room condition for 24±2 hours before initial measurement.															
	抗電強度 Dielectric Strength	按按第八條。 Per Item 8	Post-treatment: Capacitor shall be stored for 24±2 hours at room condition.															
12. 溫度迴圈 Temp. Cycling	外觀 Appearance	無顯著異常 No marked defect	將電容器放入高低溫箱, 按下列步驟迴圈 5 次。 試驗前: 電容器應放置在 125±2°C 的溫度下 1 小時, 然後在常溫下恢復 24±2 小時後測試。 試驗後: 在室內條件下恢復 24±2 小時測試。															
	容量變化 Capacitance Change	B:±10% max	The capacitor shall be introduced into the test chamber, and shall be exposed to the temperature conditions as shown in table at 5 cycles. pre-treatment: The capacitor shall be placed at 125±2°C for 1 hour, then placed at room condition for 24±2 hours before initial measurement.															
	損耗因數 D. F.	B:5.0% max	Post-treatment: Capacitor shall be stored for 24±2 hours at room conditions.															
	絕緣電阻 I. R.	1000MΩ min	<table border="1" data-bbox="767 1447 1490 1554"> <thead> <tr> <th>步驟 (STEP)</th> <th>1</th> <th>2</th> <th>3</th> <th>4</th> </tr> </thead> <tbody> <tr> <td>溫度 (TEMP.)</td> <td>-25±3°C</td> <td>20±2°C</td> <td>125±3°C</td> <td>20±2°C</td> </tr> <tr> <td>時間 (TIME)</td> <td>30±3min.</td> <td>3min. max</td> <td>30±3min.</td> <td>3min. max</td> </tr> </tbody> </table>		步驟 (STEP)	1	2	3	4	溫度 (TEMP.)	-25±3°C	20±2°C	125±3°C	20±2°C	時間 (TIME)	30±3min.	3min. max	30±3min.
步驟 (STEP)	1	2	3	4														
溫度 (TEMP.)	-25±3°C	20±2°C	125±3°C	20±2°C														
時間 (TIME)	30±3min.	3min. max	30±3min.	3min. max														
13. 耐振性 Vibration Resistance	外觀 Appearance	無顯著異常 No marked defect	<table border="1" data-bbox="775 1581 1331 1693"> <thead> <tr> <th>震動頻率 Vibration frequency</th> <th>時間 Time</th> </tr> </thead> <tbody> <tr> <td>10Hz—55Hz—10Hz</td> <td>60S</td> </tr> </tbody> </table>		震動頻率 Vibration frequency	時間 Time	10Hz—55Hz—10Hz	60S										
	震動頻率 Vibration frequency	時間 Time																
	10Hz—55Hz—10Hz	60S																
容量變化 Capacitance Change	B:±10% max	全振幅 1.5mm, 振動時間為 6 小時, 往 X·Y·Z 軸三個方向 (各 2 小時)。 試驗前: 電容器應放置在 125±2°C 的溫度下 1 小時, 然後在常溫下恢復 24±2 小時後測試。 試驗後: 在室內條件下恢復 24±2 小時測試。 With total amplitude 1.5mm, the vibration time is 6 hours, at X·Y·Z three directions each for two hours.																
損耗因數 D. F.	B:5.0% max	pre-treatment: The capacitor shall be placed at 125±2°C for 1 hour, then placed at room condition for 24±2 hours before initial measurement. Post-treatment: Capacitor shall be stored for 24±2 hours at room conditions.																



項目 ITEM	規格 SPECIFICATION		試驗方法及條件 TEST METHOD AND CONDITION
14. 易焊性 Solder ability of lead wires	導線上沾錫面積大於 90%。 Lead wire shall be soldered with uniformly coated on the axial direction over 90% of the circumferential direction.		導線須浸入助焊劑後再浸入 245±5℃的熔錫內，松香濃度 25wt，距離主體 2.0~2.5mm，時間 2±0.5 秒。 The lead wires of the capacitor shall be dipped into a alcohol solution of 25% wt rosin and then into molten solder of 245±5℃ for 2±0.5 sec. In both case the depth of dipping is up to about 2.0 to 2.5 mm from the root of the lead wires.
15. 碰撞試驗 Collision Resistance	外觀 Appearance	無顯著異常 No marked defect	電容器須焊錫固定好，固定點距電容器主體 3±1.0mm，並施加一加速度為 390m/s ² ，脈衝時間為 6ms 的碰撞，次數為 4000 次。 試驗前：電容器應放置在 125±2℃的溫度下 1 小時，然後在常溫下恢復 24±2 小時後測試。 試驗後：在室內條件下恢復 24±2 小時測試。 The capacitor shall firmly be soldered to the supporting lead wire about 3±1.0 mm from the body of the capacitor and a collision which is 390m/s ² in the acceleration, 6ms in the pulse cycle for 4000 times. pre-treatment: The capacitor shall be placed at 125±2℃ for 1 hour, then placed at room condition for 24±2 hours before initial measurement. Post-treatment: Capacitor shall be stored for 24±2 hours at room conditions.
	容量變化 Capacitance Change	B:±10% max	
16. 耐濕負荷 Humidity Resistance Loading	外觀 Appearance	無顯著異常 No marked defect	電容器在溫度 40±2℃，濕度 95±3%RH 下施加額定電壓 500(+24/-0)小時。(充放電電流<50mA) 試驗前：電容器應放置在 125±2℃的溫度下 1 小時，然後在常溫下恢復 24±2 小時後測試。 試驗後：在室內條件下恢復 24±2 小時。 The capacitor is applied a rated voltage for 500(+24/-0)hours at 40±2℃ and 95±3% RH. (Charge/discharge current<50mA) pre-treatment: The capacitor shall be placed at 125±2℃ for 1 hour, then placed at room condition for 24±2 hours before initial measurement. Post-treatment: The capacitor shall be stored for 24±2 hours at room condition.
	容量變化 Capacitance Change	B:±10% max	
	損耗因數 D. F.	B:5.0% max	
	絕緣電阻 I. R.	大於 1500MΩ 1500MΩmin	
	抗電強度 Dielectric Strength	施加 150%額定電壓 5 秒，無不良 Apply a DC voltage of 150% of the rated for 5sec, No failure.	
17. 高溫負荷 High Temperature Loading	外觀 Appearance	無顯著異常 No marked defect	施加 150%的額定電壓並在 125±2℃下放置 1000(+48/-0)小時。(充放電電流限於 50mA 以下) 試驗前：電容器應放置在 125±2℃的溫度下 1 小時，然後在常溫下恢復 24±2 小時後測試。 試驗後：在室內條件下恢復 24±2 小時。 Apply a DC voltage of 150% of the rated voltage for 1000(+48/-0) hours at 125±2℃. (Charge/discharge current≤ 50mA). pre-treatment: The capacitor shall be placed at 125±2℃ for 1 hour, then placed at room condition for 24±2 hours before initial measurement. Post-treatment: Capacitor shall be stored for 24±2 hours at room condition.
	容量變化 Capacitance Change	B:±10% max	
	損耗因數 D. F.	B:4.0% max	
	絕緣電阻 I. R.	大於 4000MΩ 4000MΩmin	
	抗電強度 Dielectric Strength	施加 150%額定電壓 5 秒，無不良 Apply a DC voltage of 150% of the rated for 5sec, No failure.	
18. 加壓蒸煮 Pressure Cooking	外觀 Appearance	無顯著異常 No marked defect	電容器在溫度 121±2℃，2 個大氣壓下放置 2 小時，濕度 100%RH。 試驗前：在 125±2℃放置 1 小時，在室內條件下恢復 24±2 小時。 試驗後：在室內條件下恢復 24±2 小時。 Set the capacitor for 2 hours at 121±2℃ in 2 atmospheres, 100%RH. Pre-treatment: Capacitor shall be stored at 125±2℃ for 1 hour, then placed at room condition for 24±2H before initial measurements. Post-treatment: Capacitor shall be stored for 24±2 hours at room condition.
	絕緣電阻 I. R.	大於 1500MΩ 1500MΩ min	
	抗電強度 Dielectric Strength	施加 150%額定電壓 5 秒，無不良 Apply a DC voltage of 150% of the rated for 5sec, No failure.	

□ 編帶包裝 (Taping Package packing)

1、包裝方式 (over-wrap packing shape)



2、包裝數量 (packing quantity):

產品間距 Product spacing	成型方式 Molding mode	包裝數量(只) Quantity per bag (pcs)	箱裝數量(只) Quantity per box (pcs)
P=P0=15.0mm	b	1000~2000	5000~10000/10000~20000

3、包裝標識 (packing marking):

最小包裝標識 (Minimum package label)	外包裝箱標識 (Taping Package Carton logo)																		
	<table border="1"> <thead> <tr> <th>品名</th> <th colspan="2">电容器</th> </tr> </thead> <tbody> <tr> <td>规格型号</td> <td colspan="2">CT81-6KV-07b-2B4-151K-1T1</td> </tr> <tr> <td>料号</td> <td colspan="2"></td> </tr> <tr> <td>数量</td> <td colspan="2">10000PCS</td> </tr> <tr> <td>生产日期</td> <td colspan="2">2018.11.26</td> </tr> <tr> <td>成型代号</td> <td>5b</td> <td>备注</td> </tr> </tbody> </table>	品名	电容器		规格型号	CT81-6KV-07b-2B4-151K-1T1		料号			数量	10000PCS		生产日期	2018.11.26		成型代号	5b	备注
品名	电容器																		
规格型号	CT81-6KV-07b-2B4-151K-1T1																		
料号																			
数量	10000PCS																		
生产日期	2018.11.26																		
成型代号	5b	备注																	

裝箱數量應為最小包裝的整數倍。(The packing quantity should be integral multiple of minimum packing.)

採用瓦楞紙箱包裝，每箱裝 5/10 盒編帶產品，裝箱不滿時用空箱填充。(The corrugated box packaging, Each box 10 boxes of Taping products, packing dissatisfaction with empty container filling)。

□瓷介電容器使用注意事項 Ceramic capacitor use matters needing attention :

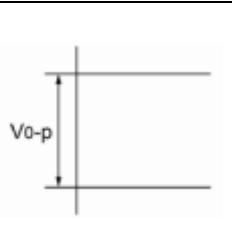
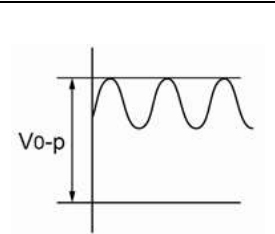
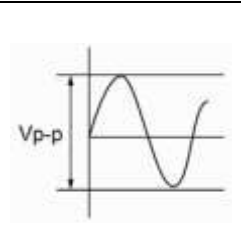
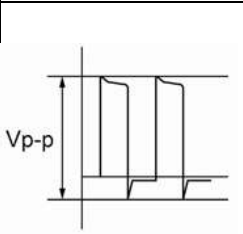
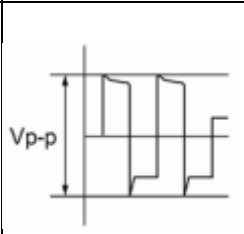
1. 工作電壓 Operating voltage :

在交流電路或紋波電流電路中使用直流額定電壓電容器時, 請務必確保外加電壓的 V_{p-p} 值或包含直流偏置電壓的 V_{o-p} 值保持在額定電壓範圍內.

若向電路施加電壓, 開始或停止時可能會因諧振或切換產生暫時的不規則電壓. 請務必使用額定電壓範圍大於此不規則電壓的電容器.

Be sure to maintain the V_{p-p} value of the applied voltage or the V_{o-p} which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.

直流電壓 DC voltage	直流+交流電壓 DC+AC voltage	交流電壓 AC voltage	衝擊電壓 (1) Pulse voltage(1)	衝擊電壓 (2) Pulse voltage(2)
				

2. 工作溫度和自身發熱 Operating temperature and self-generated heat:

電容器的表面溫度應保持在其額定工作溫度範圍的上限以下. 務必考慮到電容器自身發出的熱量. 電容器在高頻電流、衝擊電流等中使用時可能會因介電損耗自身發熱. 所施加之正弦波電壓的頻率應低於300kHz. 外加電壓應使自身發熱等負荷在25°C周圍溫度條件下不超過20°C範圍. 測量時應使用 $\phi 0.1\text{mm}$ 小熱容量的 (K) 的熱電偶, 而且電容器不應受到其它元件的散熱或周圍溫度波動影響.

過熱可能會導致電容器特性及可靠性下降. (切勿在冷卻風扇運轉時進行測量, 否則無法確保測量資料的精確性.)

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself. When the capacitor is used in a high-frequency current, pulse current or the like, it may have the self-generated heat due to dielectric-loss. Applied voltage should be the load such as self-generated heat is within 20°C on the condition of atmosphere temperature 25°C. When measuring, use a thermocouple of small thermal capacity-K of $\phi 0.1\text{mm}$ and be in the condition where capacitor is not affected by radiant heat of other components and wind of surroundings.

Excessive heat may lead to deterioration of the capacitor's characteristics and reliability. (Do not measure when the cooling fan is running, otherwise unable to ensure the accuracy of the measurement data.)

3. 耐電壓的測試條件 Test condition for withstanding voltage :

3.1 測試設備 Test equipment :

交流耐電壓的測試設備應具有能夠產生類似於50/60Hz正弦波的功能, 如果施加變形的正弦波或超過規定電壓值的超載電壓, 則可能會導致故障.

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

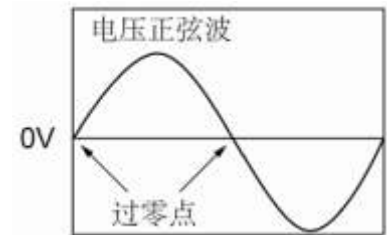
3.2 電壓外加方法Voltage applied method :

施加耐電壓時，電容器的引線或端子應與耐電壓測試設備的輸出端連接牢固，然後再將電壓從近零增加到測試電壓。

如果測試電壓不從近零逐漸提高而是直接施加在電容器上，則施加時應包含過零點*。測試結束時，測試電壓應降到近零，然後再將電容器引線或端子從耐電壓測試設備的輸出端取下。

如果測試電壓不從近零逐漸提高而是直接施加在電容器上，則可能會出現浪湧電壓，從而導致故障。

*過零點是指電壓正弦通過0V的位置。參見右圖：



When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the *zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.

ZERO CROSS is the point where voltage sine wave pass 0V. - See the right figure.

4. 失效安全性Fail-Safe :

如果電容器破損，會導致短路電路故障。務必在本產品上適當提供例如保險絲等自動防故障功能，以免導致電擊、火災、或冒煙等。

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

5. 儲存環境Storage condition :

電容器的絕緣塗層不具有良好密封作用。因此，請勿將電容器存放在腐蝕性氣體中，尤其是存在氯氣、硫氣、酸、堿、鹽等的場所。同時應防潮。在對本產品進行清洗、覆膜或封膜前，請先在指定設備上測試經清洗、覆膜或封膜的產品的性能，以確定上述過程不會影響產品品質。電容器應存放在下列條件的場所：

環境溫度：-25℃——+50℃；相對濕度：40℃時不大於90%；氣壓：40——106KPA

請在6個月內使用電容器。

使用本品時如忽略上述警告事項，則在嚴重情況下可能導致短路，並引起冒煙或局部離散。

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Capacitors should be stored in the following conditions:

The environment temperature : -25℃~+50℃; Relative humidity : 40 °C is not greater than 90%

Air pressure : 40~106KPA

Use capacitors within 6 months.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

6. 焊接、安裝與使用Soldering、mounting and use:

6.1 振盪與衝擊Vibration and impact

使用時請勿使電容器或引線受到過度衝擊或振盪.

When using do not make the capacitor or lead by excessive impact or vibration.

6.2 焊接Soldering

將該產品焊接在PCB/PWB上時,不應超出電容器的耐焊接熱規格,本產品過熱會使內部接點錫焊料熔化,導致溫度驟變,從而造成陶瓷元件產生裂紋.

當使用烙鐵焊接電容器時,應遵循以下條件:

烙鐵頭溫度: 最高400°C;烙鐵功率: 最大50W;焊接時間: 最多3.5秒

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder, Lead to temperature shock, Resulting in the ceramic element to produce crack.

When soldering capacitor with a soldering iron, it should be performed in following conditions:

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

7. 清洗(超聲波清洗) Cleaning (ultrasonic cleaning):

進行超聲波清洗時,應遵守下列條件:

洗滌槽容量: 20瓦特輸出功率/每升或以下;洗滌時間: 最多5分鐘;

不得直接振盪PCB/PWB. 過度的超聲波洗滌會導致引線的超載損壞.

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less. Rinsing time: 5 minutes max.

Do not vibrate the PCB/PWB directly. Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires

運輸Transport :

電容器在運輸途中,要注意避免陽光直射,雨、雪、霧、水浸透等.

In transit, Capacitors should be avoided direct sunlight, rain, snow, fog, water soaked and so on.