



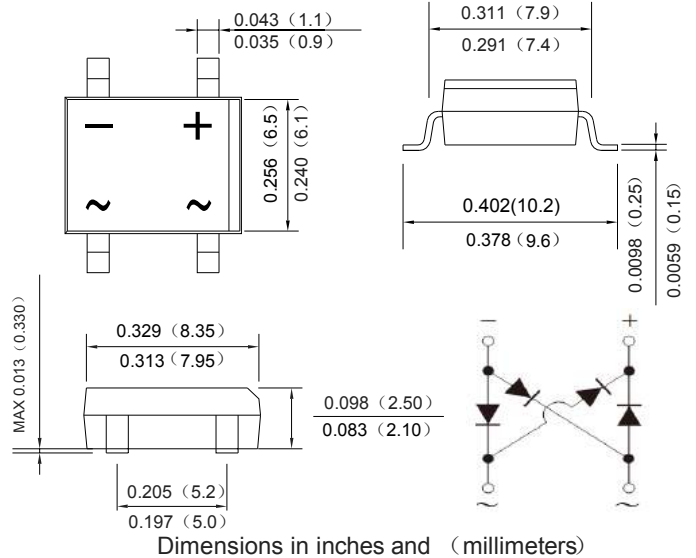
DB201S THRU DB207S

SINGLE PHASE 2.0AMP SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

Features

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Designed for surface mount application
- Plastic material-UL flammability 94V-0

DB-S



Mechanical Data

- Case: DB-S, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any
- Marking: type number
- Lead Free: For RoHS / Lead Free Version

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single Phase, half wave, 60Hz, resistive or inductive load.

For capacitive load, derate current by 20%.

TYPE NUMBER	SYMBOL	DB201S	DB202S	DB203S	DB204S	DB205S	DB206S	DB207S	UNITS	
Peak Repetitive Reverse Voltage	V _{RRM}									
Working Peak Reverse Voltage	V _{RWM}	50	100	200	400	600	800	1000	V	
DC Blocking Voltage	V _{DC}									
RMS Reverse Voltage	V _{RMS}	35	70	140	280	420	560	700	V	
Average Rectified Output Current (Note 1)@T _c =100°C	I _{F(AV)}	2.0								A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I _{FSM}	60								A
I ² t Rating for Fusing (t < 8.3ms)	I ² t	14.94								A ² s
Forward Voltage per element @I _F =2.0A	V _{FM}	1.0								V
Peak Reverse Current @T _J =25°C At Rated DC Blocking Voltage @T _J =125°C	I _R	5.0 100								uA
Typical Junction Capacitance (Note 2)	C _J	25								pF
Typical Thermal Resistance	R _{θJA}	40								°C/W
	R _{θJL}	15								
Operating and Storage Temperature Range	T _J , T _{STG}	-55to+150								°C

Note:1. Mounted on glass epoxy PC board with 1.3mm² solder pad.

2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.



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Fig. 1 Output Current Derating Curve

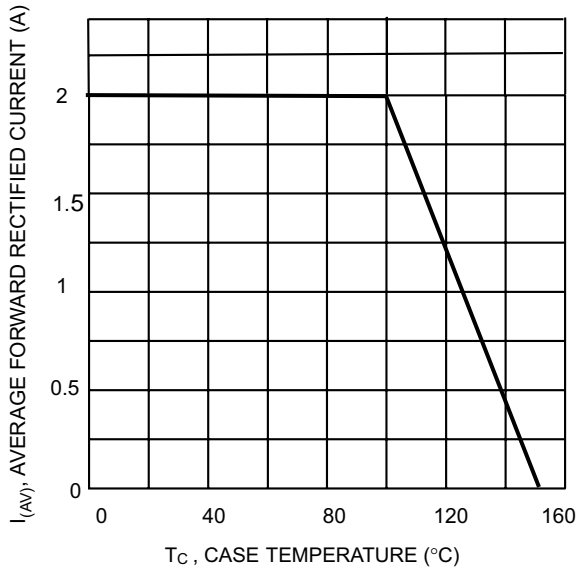


Fig. 2 Typical Forward Characteristics

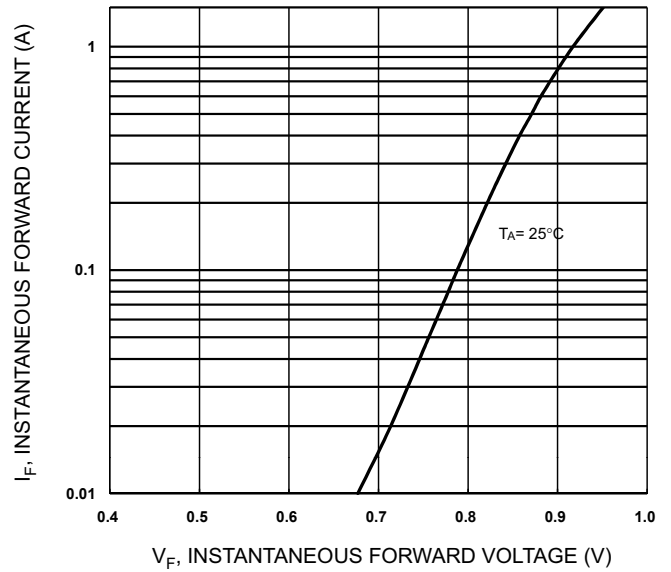


Fig. 3 Maximum Peak Forward Surge Current

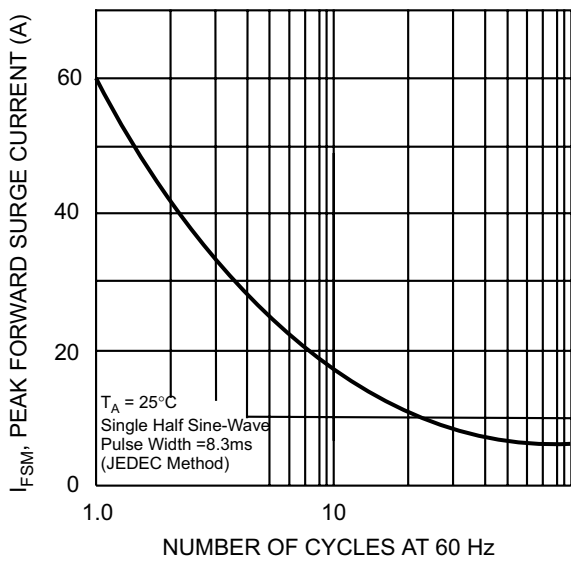


Fig. 4 Typical Reverse Characteristics

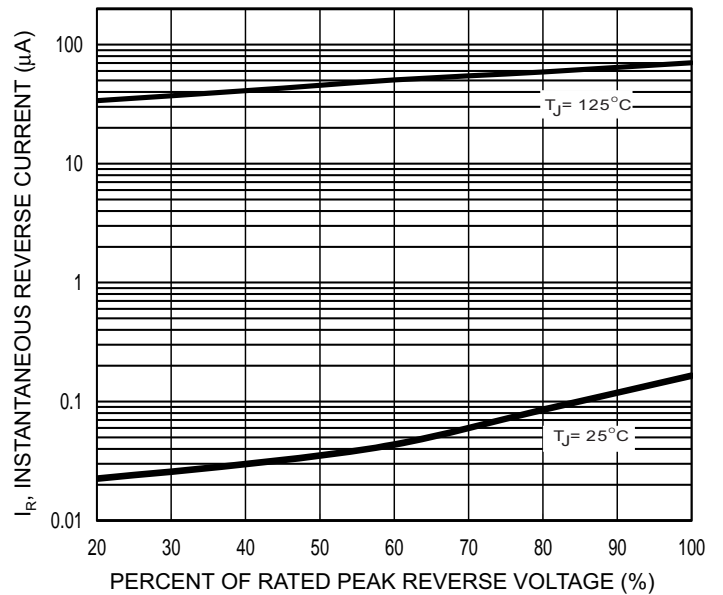


Fig. 5 Typical Junction Capacitance

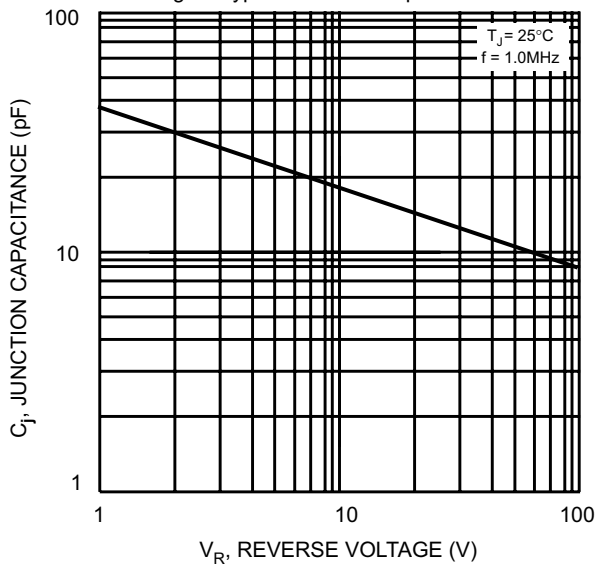
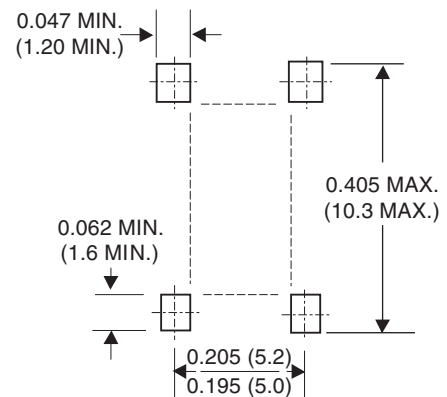


FIG.6 MOUNTING PAD LAYOUT





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